**TOPICS TO STUDY FOR THEORY QUIZ #6**

**Text Book -Chapter 9 (Main Memory)**

9.1 Background

9.1.1 Basic Hardware

9.1.3 Logical versus physical address space

9.2.1 Memory Protection

9.3 Paging

9.3.1 Basic Method

9.3.2 Hardware Support

9.3.3 Protection

9.3.4 Shared pages

9.4 Structure of the Page Table

9.4.1 Hierarchical Paging

9.6 Example: Intel 32 and 64-bit Architecture

9.6.1 IA-32 Architecture

9.6.2 x86-64

9.7 Example: ARMv8 Architecture

Slides and class Notes